



WIFIKIT

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Centralus™

SKU: WIFIKIT

Centralus ICC2 Wi-Fi

Shipping Information

UPC-A: 0611698029039

A diagram of a 5-bit shift register. It consists of five rectangular cells connected in a horizontal line. The first cell on the left has an input labeled 'A' and an output labeled 'A'. The second cell has an input labeled 'B' and an output labeled 'B'. The third cell has an input labeled 'C' and an output labeled 'C'. The fourth cell has an input labeled 'D' and an output labeled 'D'. The fifth cell on the right has an input labeled 'E' and an output labeled 'E'. Arrows indicate the flow of data from the inputs into the cells and from the cells to the outputs.

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Additional Information

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☐ With Flow

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